



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Nandakumar et al. Examiner: Edgardo Ortiz
Serial No.: 09/876,292 Art Unit: 2815
Filed: 06/07/2001 Docket: TI-31089

For: ADDITIONAL N-TYPE LDD/POCKET IMPLANT FOR IMPROVING
SHORT-CHANNEL NMOS ESD ROBUSTNESS

2815
#10
formal
draws
4-18-03
G.C.

March 26, 2003

Commissioner for Patents
and Trademarks
Washington, D.C. 20231

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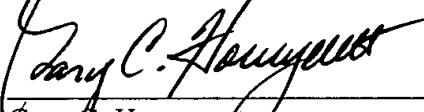
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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents and Trademarks, Washington, D.C. 20231, on the date shown below.	
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<u>Ginger Cox</u> Ginger Cox	

Sir:

SUBSTITUTE DRAWINGS

Further to our facsimile of March 19, 2003, and responsive to the initial telephone call from Examiner Ortiz, enclosed for filing in this case are the original formal drawings for substitution.

Respectfully submitted,


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Reg. No. 20,250

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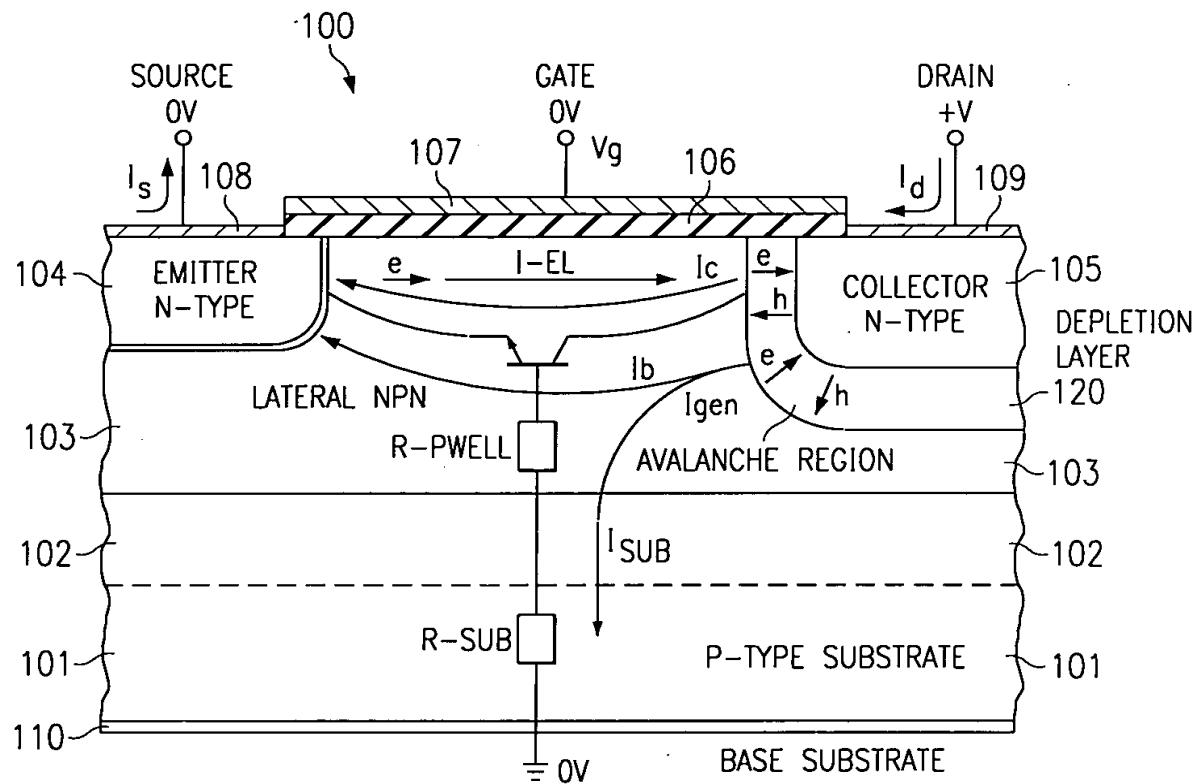


FIG. 1
(*PRIOR ART*)

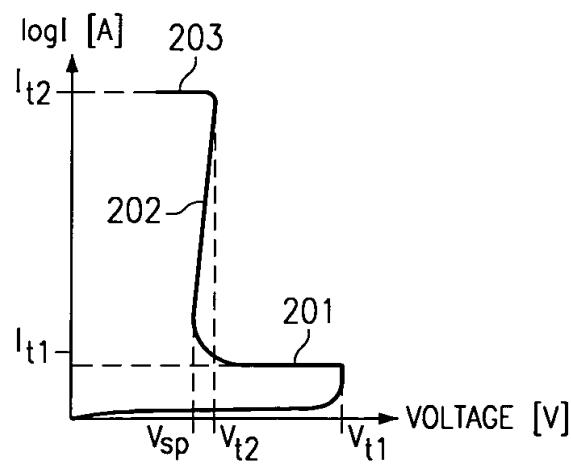


FIG. 2
(*PRIOR ART*)



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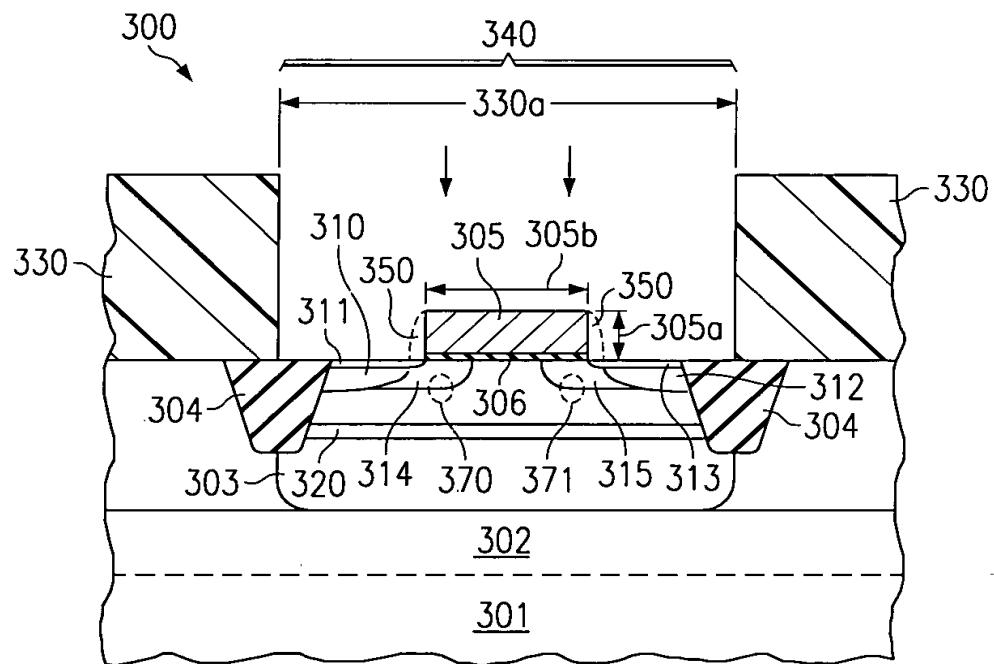


FIG. 3

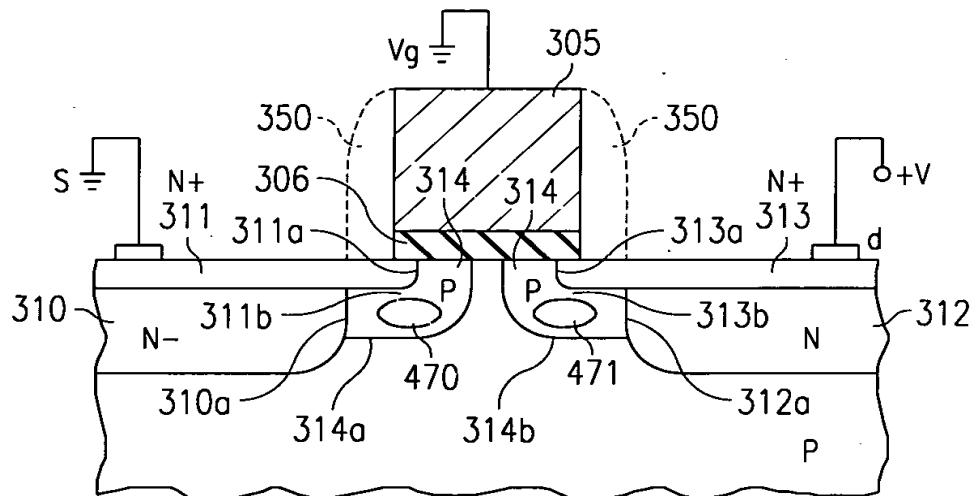


FIG. 4



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FIG. 5

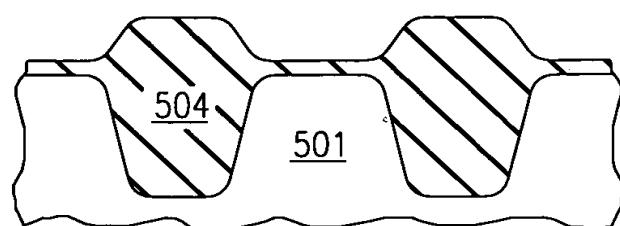


FIG. 6

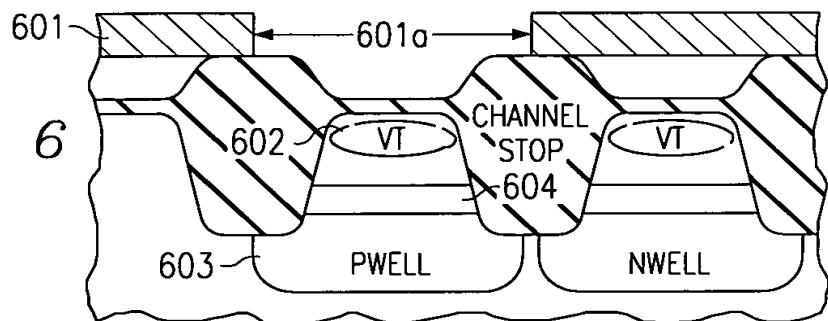


FIG. 7

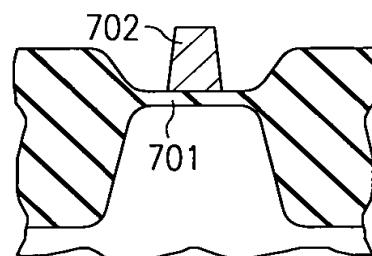


FIG. 8

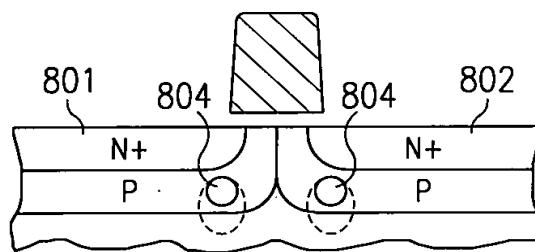


FIG. 9

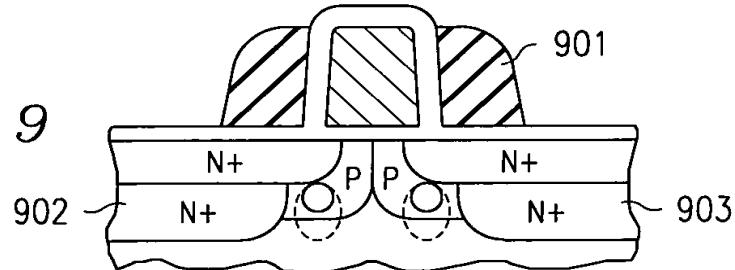


FIG. 10

